

# ELCE 2013

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## Secure Embedded Linux Product (A Success Story)

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- **Marcin Bis**
- Entrepreneur
- Embedded Linux: system development, kernel development.
- Esp. Linux + Real-Time - automation (industrial- and home-).



I want to tell you about a success story. . .  
. . . protecting **added value** in a product.

- A few quick words about security
  - Embedded security.
  - Attack vector and surface.
  - What is similar with standard system security?
- Practical example - secured embedded Linux system.
  - A problem (business view).
  - Active & passive security.
  - Examples.

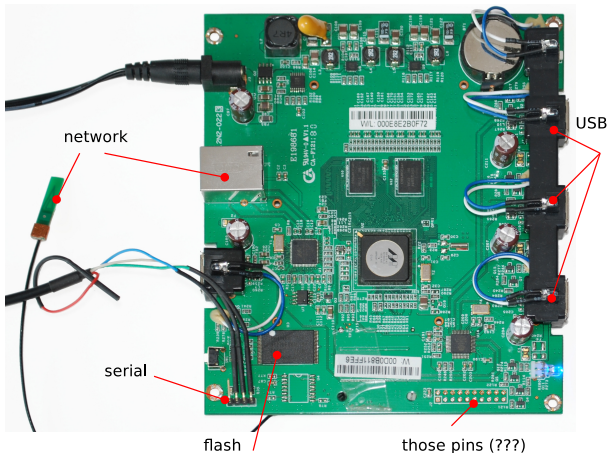
**I will not talk about::**

- Android
- Web apps, dedicated apps, cloud.

# Attack surface

One or more input methods of the system.

- which can be accessed by untrusted user,
- or access to which can be influenced.



... to exploit a surface. Common ones:

- network (TCP/IP, Wi-Fi),
- application,
- serial port.

Less obvious:

- USB,
- I2C,
- solid state memory (FLASH),
- Bluetooth
- GPS, cellular network.

Less obvious == more dangerous.

Some differences:

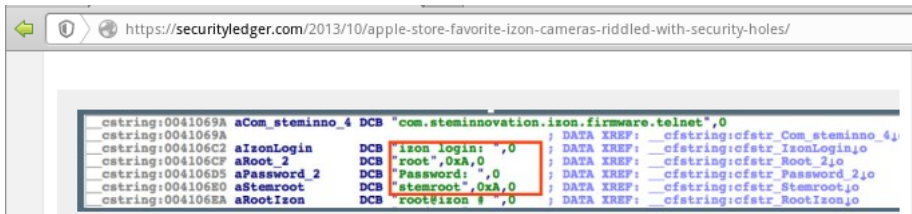
- Some attack vectors are unique to embedded devices.
- Problematic updates (software monoculture).
- People do not threat them as devices.

On the other hand - same programs and services.

(Wireless) network accessible.

Apache, openssh, perl, avahi, dns, openssl etc.

- Stuxnet
- FTP access to / via root account.
- admin:default - common in network devices.
- More, easy to find: <http://lwn.net/talks/elc2009/> (2009).
- Another example of hard-coded credentials:



The screenshot shows a web browser window with the address bar displaying <https://securityledger.com/2013/10/apple-store-favorite-izon-cameras-riddled-with-security-holes/>. Below the browser window, a hex dump of firmware data is displayed. The data is organized into columns: memory addresses, labels, data values, and cross-references (XREFs). A red rectangle highlights the following entries:

Address	Label	Data	XREF
0041069A	aCom_steminno_4	com.stemininnovation.izon.firmware.telnet,0	
0041069A		izon_login: ",0	cfstring:cfstr_Com_steminno_4,
004106C2	aIzonLogin	"root",0xA,0	cfstring:cfstr_IzonLogin,
004106CF	aRoot_2	"Password: ",0	cfstring:cfstr_Root_2,
004106D5	aPassword_2	stemroot",0xA,0	cfstring:cfstr_Password_2,
004106E0	aStemroot	root@izon # ",0	cfstring:cfstr_Stemroot,
004106EA	aRootIzon		cfstring:cfstr_RootIzon,

Common methods are easy to avoid:

- Restricted shell access, eg. serial port
  - strong password,
  - use PAM to auto-logout idle shells.
- Other access methods to shell (web shell, ssh, telnet (!) etc.)
- Strong passwords (+1).
- Do not run all applications from root account.
- Bug-fix-ed components.
- Self developed vs. standard software.
  - Defensive programming.



# Passive security

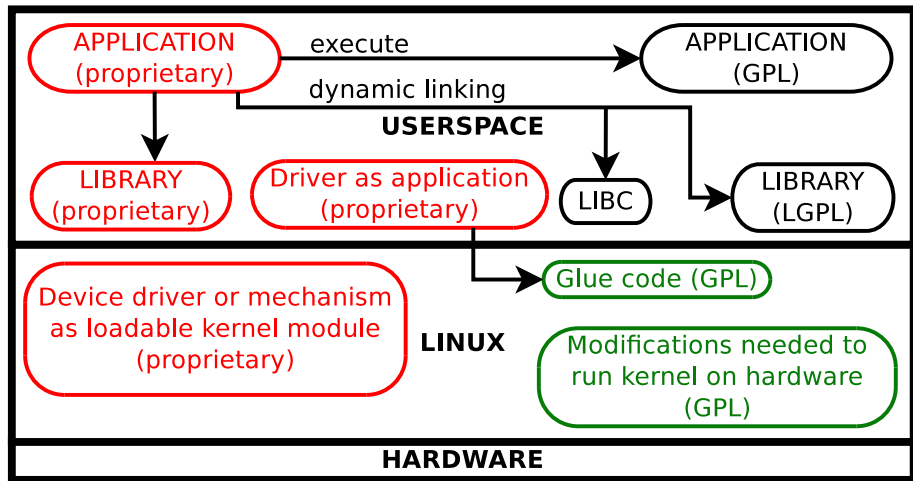
**Added Value**

**Free Software | Open Source**

# How my customer see the product?



- Hardware becomes cheaper and cheaper.
- Expectations increases (let's add functionality).
- Linux and open source is a foundation of the **software product**.
- Open-Source and Free Software gives us all freedom:
  - Every developer has the same rights.
  - And equal chances.
- Customer will make money on **added value**
  - According to licences of course:
    - GPL
    - LGPL
    - BSD



# How to secure a **added value**?

- possibility of "TiVo-lization", - do not go to far.
- GPLv3

At first:

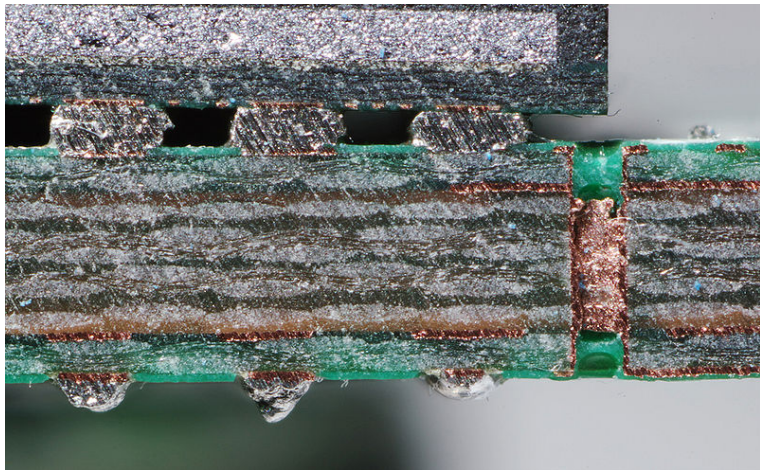
- Nothing will stop user (abuser) from de-soldering an element and trying to analyze logic states.
- Most SoC-s has hundreds of pins - it is difficult (but not impossible).

It all depends on how determined you are (\$\$\$).

Security is a process not a product.

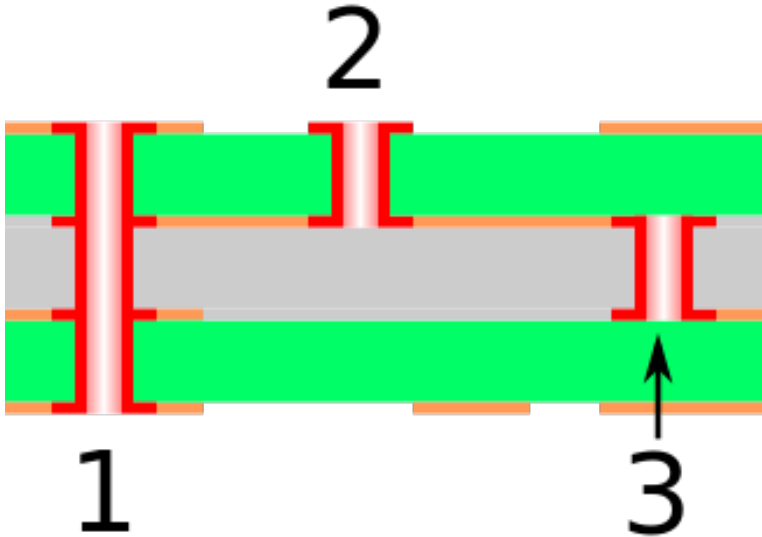
- BGP - it is harder to analyze data on bus,
- inner layers of PCB are harder to access,
- of using Application processor and external uC - add some logic to check timing (like watchdog).
- TPM chips.

VIA



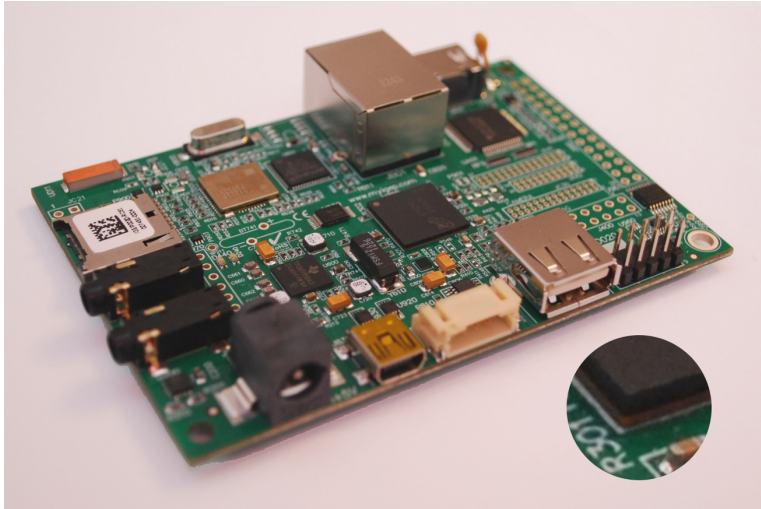
(Wikipedia)





(Wikipedia)

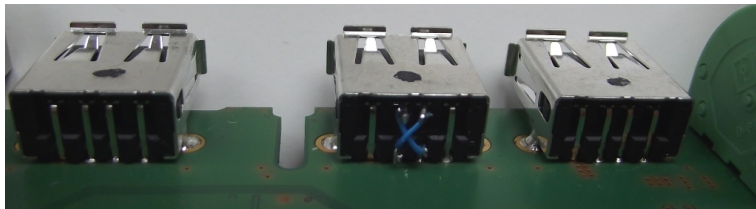
# a sandwich



(Wikipedia)

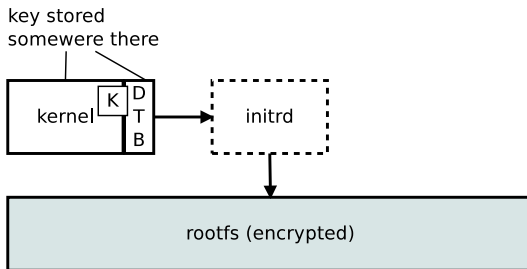
# The problem...

- It is not easy to debug firmware.



# Let's secure data

- Sign it.
  - TPM
  - HAB
- or **encrypt it**
  - Should be fast.
  - Performance penalty (esp. Real-Time).
  - Where to store the key.



Block devices (e.g. eMMC):

- dm-crypt
- `man cryptsetup`
- LUKS

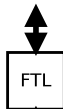
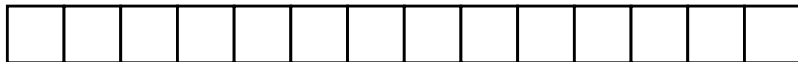
Any filesystem

- ecryptfs
- `sudo mount -t ecryptfs tmp1 tmp2`
- problems using on rootfs (pivot\_root, switch\_root)
- still, can be used to encrypt parts of filesystem.

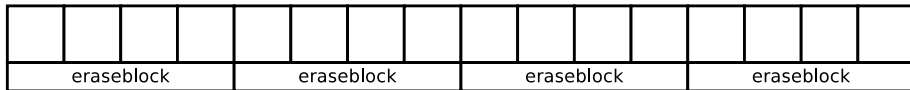
Cutomer wants to have a raw NAND device (wear leveling).

## How does it work?

Block device

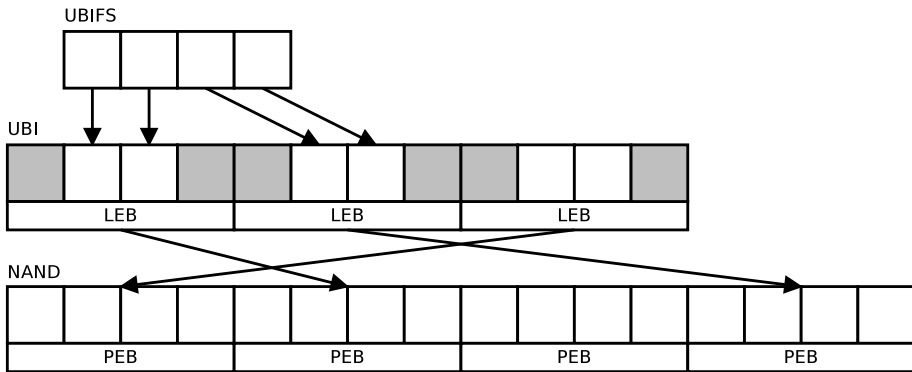


NAND



NAND

A1	B1	C1	A2	A3	A4	C2	B2	B3	C3	BAD	A5	A6			
eraseblock				eraseblock				eraseblock				eraseblock			





...for big NANDs:

- [http://elinux.org/Flash\\_FileSystem\\_Benchmarks](http://elinux.org/Flash_FileSystem_Benchmarks)

## How to add encryption?

- It can emulate block device.
- Use ecryptfs.
- **look at the source code.**

UBIFS already compresses data it writes. Maybe it could encrypt it too.

- Using Crypto-API.

This patch adds a function to perform AES encryption. The compress and decompress routines use this function if they are called with a non-NULL key parameter. It uses AES counter mode (where encryption and decryption are the same function) and performs the operation in place on the data. It uses a default IV of 0, since each key is only ever used to encrypt one data item the IV does not matter.

The const qualifier was removed from the decompress routine for the following reason. Encrypted data is not compressable, so compression is first applied then the result is encrypted. In the reverse, decryption is first applied and the result decompressed. This means that either the input buffer for decompression is used to perform an in-place decryption before decompression, or a third buffer is added and data is copied around.

Signed-off-by: Joel Reardon <[reardonj@inf.ethz.ch](mailto:reardonj@inf.ethz.ch)>

```
---
fs/ubifs/compress.c | 77 ++++++-----
fs/ubifs/ubifs.h    | 12 +-----
2 files changed, 85 insertions(+), 4 deletions(-)
```

On the other side:

File Edit View Terminal Go Help

.config - Linux/arm 3.10.0 Kernel Configuration

> Device Drivers > Staging drivers

Support for the DCP engine

CONFIG\_CRYPTO\_DEV\_FSL\_DCP:

Say 'Y' here to use the DCP AES and SHA engine for the CryptoAPI algorithms.

To compile this driver as a module, choose M here: the module will be called fsl-dcp.

Symbol: CRYPTO\_DEV\_FSL\_DCP [=y]

Type : tristate

Prompt: Support for the DCP engine

Location:

-> Device Drivers

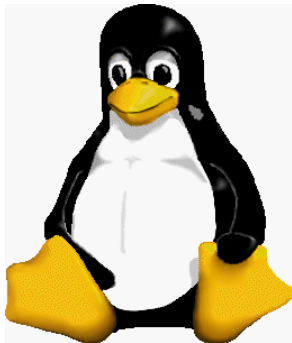
-> Staging drivers (STAGING [=y])

Defined at drivers/staging/crypto/Kconfig:1

( 66%)

< Exit >

# Use proper block cipher



data

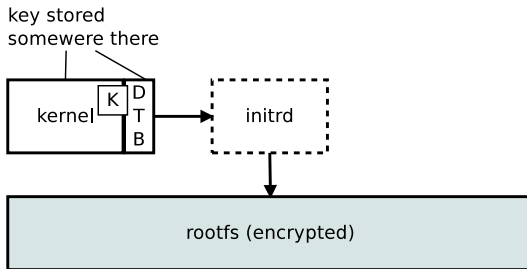


AES-ECB



AES-CTR

```
openssl enc -aes-128-ecb -k "secret" -in logo.ppm -out out.ppm
```



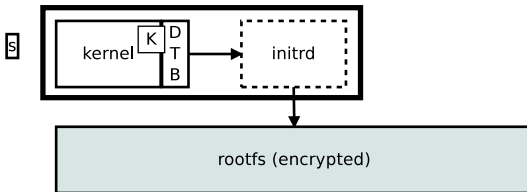
The problem of storing encryption key - still exists.

The problem of storing encryption key - still exists.

- Put it as DT attribute.
- Modify NAND driver to use it.

Encrypt kernel+DT using functions of the Chip.

## i.MX28 SecureBoot



## What is important?

Security is not a **product**.

it is a **process**.

## What else?

Internal attacks.

I do not even trust myself.

# Questions?